



# Computer Engineering

## CE Program Seminar



### Professor Tetsuo Asano

School of Information Science, Japan Advanced Institute of Science and Technology

TUESDAY, MARCH 7, 2006 ~ 2:00 pm ~ Engineering I, Room 2162

#### *Algorithmic Approaches to Digital Halftoning*

Digital halftoning is a technique to convert a continuous-tone image into a binary image consisting of black and white dots. It is an important technique for printing machines and printers to output an image with few intensity levels or colors which looks similar to an input image.

In this talk Professor Asano will explain how algorithms can contribute to digital halftoning or what geometric and combinatorial problems are related to digital halftoning.

#### Speaker Bio

Tetsuo Asano received his Bachelor, Master and Ph.D. degrees all from Osaka University, Japan.

He is now a professor at JAIST, Japan. His interests are in VLSI layout design, computational geometry and its applications to practical problems. He serves as an editor for three international journals on computational geometry (Discrete and Computational Geometry, Computational Geometry: Theory and Applications, International Journal on Computational Geometry and Applications). He is an ACM fellow.

